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	LVANIA AVENUE, N	CHANG, ERIC		
WASHINGTO	N, DC 20037		ART UNIT	PAPER NUMBER
			2116	
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# Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

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Office Action Communication		Арр	lication No.	Applicant(s)	Applicant(s)		
		10/5	587,022	KOHIGA, AKIHIT	KOHIGA, AKIHITO		
Office Action Summary			miner	Art Unit			
		ERIC	C CHANG	2116			
Period fo	The MAILING DATE of this communic r Reply	ation appears o	on the cover sheet with	the correspondence a	ddress		
WHIC - Exter after - If NO - Failu Any r	ORTENED STATUTORY PERIOD FO CHEVER IS LONGER, FROM THE MAN ISSUME IS LONGER, FROM THE MAN ISSUME ISS	ILING DATE ( 37 CFR 1.136(a). In nication. Itory period will apply ill, by statute, cause	OF THIS COMMUNICA in no event, however, may a repl or and will expire SIX (6) MONTH the application to become ABAN	ATION.  y be timely filed  IS from the mailing date of this of the second state of the			
Status							
2a)⊠		)∏ This actio	n is non-final.				
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Dispositi	on of Claims						
5)□ 6)⊠ 7)□	Claim(s) <u>1-35</u> is/are pending in the ap 4a) Of the above claim(s) is/are Claim(s) is/are allowed.  Claim(s) <u>1-35</u> is/are rejected.  Claim(s) is/are objected to.  Claim(s) are subject to restricti	withdrawn fro					
Applicati	on Papers						
10)	The specification is objected to by the The drawing(s) filed on is/are: Applicant may not request that any object Replacement drawing sheet(s) including the oath or declaration is objected to be	a) accepted ion to the drawir he correction is	g(s) be held in abeyance required if the drawing(s)	e. See 37 CFR 1.85(a). is objected to. See 37 C	, ,		
Priority u	ınder 35 U.S.C. § 119						
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>							
Attachmen	t <b>(s)</b> e of References Cited (PTO-892)		4) ☐ Interview Sur	nmary (PTO-413)			
2)  Notic 3) Inforr	e of Draftsperson's Patent Drawing Review (PT nation Disclosure Statement(s) (PTO/SB/08) r No(s)/Mail Date	O-948)	Paper No(s)/I	Mail Date rmal Patent Application			

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### **DETAILED ACTION**

1. Claims 1-35 are pending.

## Claim Rejections - 35 USC § 102

- 2. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
- 3. Claims 1-35 are rejected under 35 U.S.C. 102(e) as being anticipated by U.S. Patent 7,373,496 to Sekiguchi et al.
- 4. As to claim 1, Sekiguchi discloses a rapid restart method comprising: saving, before restart of an OS, process information in the OS relating to a user process [col. 13, lines 14-52, and col. 9, line 39—col. 10, line 5] to a save area on a main memory device [500]; initializing, at the restart of the OS, a main memory area of the main memory device used by the OS while not restarting the save area used by the user process [col. 10, lines 6-10]; and restoring the saved process information in the OS after the restart of the OS [col. 10, line 29 col. 11, line 35].
- 5. As to claim 2, Sekiguchi discloses a rapid restart method comprising: saving, before restart of an OS, process information in the OS relating to a user process to a save area on a main memory device [col. 13, lines 14-52, and col. 9, line 39—col. 10, line 5], while setting a restart flag for the saved process information to designate whether the process is to be restarted or not [col. 10, lines 14-20]; initializing, at the restart of the OS, a main memory area of the main

memory device used by the OS while not restarting the save area used by the user process for which the restart flag is set not to restart [col. 10, lines 6-10]; and restoring the saved process information of the user process for which the restart flag is set not to restart in the OS, after the restart of the OS [col. 10, line 29 – col. 11, line 35].

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- 6. As to claim 3, Sekiguchi discloses a rapid restart method comprising: saving, before restart of an OS, process information in the OS relating to a user process to be continuously operated after restart of the OS, to a save area on a main memory device [col. 13, lines 14-52, and col. 9, line 39—col. 10, line 5]; initializing, at the restart of the OS, a main memory area of the main memory device used by the OS while not restarting the save area used by the user process [col. 10, lines 6-10]; and restoring the saved process information in the OS, after the restart of the OS [col. 10, line 29 col. 11, line 35].
- 7. As to claim 4, Sekiguchi discloses a rapid restart method comprising: saving, before restart of an OS, process information in the OS relating to a user process to be continuously operated after restart of the OS, to a save area on a main memory device by referring to a process ID table in which an identifier for a process to be continuously operated or a process not to be continuously operated after restart of the OS [col. 13, lines 14-52, and col. 9, line 39—col. 10, line 5]; initializing, at the restart of the OS, a main memory area of the main memory device used by the OS while not restarting the save area used by the user process [col. 10, lines 6-10]; and restoring the saved process information in the OS, after the restart of the OS [col. 10, line 29 col. 11, line 35].

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8. As to claim 5, Sekiguchi discloses a rapid restart method comprising: saving, at generation of a user process, process information in the OS relating to the generated user process to a save area on a main memory device [col. 13, lines 14-52, and col. 9, line 39—col. 10, line 5]; setting, at switching of the user process, a restart flag for the saved process information to designate whether the process is to be restarted or not, while updating the process information saved in the save area to the latest state if the process is not to be restarted [603]; nullifying the saved process information, at termination of the user process [1004]; initializing, at restart of an OS, a main memory area of the main memory device used by the OS while not restarting the save area used by the user process for which the restart flag is set not to restart [col. 10, lines 6-10]; and restoring, after the restart of the OS, the saved process information of the user process for which the restart flag is set not to restart in the OS [col. 10, line 29 – col. 11, line 35].

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- 9. As to claim 6, Sekiguchi discloses when a restart flag is set for process information relating to a certain user process to designate whether the process is to be restarted or not, all the user processes belonging to the same user application program as the user process are searched, and restart flags in the process information relating to all the searched user processes are also set to the same value [col. 8, lines 19-37].
- 10. As to claim 7, Sekiguchi discloses when a restart flag is set for process information relating to a certain user process to designate whether the process is to be restarted or not, all the user processes belonging to the user application program as the user process are searched, and

restart flags in the process information relating to all the searched user processes are also set to the same value [col. 8, lines 19-37].

- 11. As to claim 8, Sekiguchi discloses the OS is started up from an OS main memory image stored in a nonvolatile storage portion forming a part of the main memory device [FIG. 1, "REBOOT LOADER" in main memory device 102].
- 12. As to claim 9, Sekiguchi discloses every time occurs a write access from the OS to the nonvolatile storage portion during system operation, data in an address range having a predetermined width including the address at which the write access has occurred is copied from the nonvolatile storage portion to a substitute area ensured in a readable/writable main memory portion forming a part of the main memory device, and subsequent accesses to the address range having the predetermined width are converted to accesses to the substitute area [col. 9, line 39—col. 10, line 5].
- 13. As to claim 10, Sekiguchi discloses an information processing apparatus comprising: process information saving means for saving, before restart of an OS, process information relating to a user process to a save area on a main memory device [col. 13, lines 14-52, and col. 9, line 39—col. 10, line 5]; main memory initialization means for initializing, at the restart of the OS, a main memory area of the main memory device used by the OS while not restarting the save area used by the user process [col. 10, lines 6-10]; and process restoration means for

restoring the saved process information in the OS, after the restart of the OS [col. 10, line 29 – col. 11, line 35].

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- 14. As to claim 11, Sekiguchi discloses an information processing apparatus comprising: process information saving means for saving, before restart of an OS, process information relating to a user process to a save area on a main memory device [col. 13, lines 14-52, and col. 9, line 39—col. 10, line 5]; restart flag setting means for setting a restart flag for the saved process information to designate whether the process is to be restarted or not [col. 10, lines 14-20]; main memory initialization means for initializing, at the restart of the OS, a main memory area of the main memory device used by the OS while not restarting the save area used by the user process for which the restart flag is set not to restart [col. 10, lines 6-10]; and process restoration means for restoring, after the restart of the OS, the saved process information of the user process for which the restart flag is set not to restart in the OS [col. 10, line 29 col. 11, line 35].
- 15. As to claim 12, Sekiguchi discloses an information processing apparatus comprising: process information saving means for saving, before restart of an OS, process information in the OS relating to a user process to be continuously operated after restart of the OS, to a save area on a main memory device [col. 13, lines 14-52, and col. 9, line 39—col. 10, line 5]; main memory initialization means for initializing, at the restart of the OS, a main memory area of the main memory device used by the OS while not restarting the save area used by the user process [col.

10, lines 6-10]; and process restoration means for restoring the saved process information in the OS, after the restart of the OS [col. 10, line 29 – col. 11, line 35].

- 16. As to claim 13, Sekiguchi discloses before the restart of the OS, the process information saving means saves process information in the OS relating to a user process to be continuously operated after restart of the OS, to the save area on the main memory device, by referring to a process ID table storing identifiers of processes to be continuously operated or of processes not to be continuously operated [col. 13, lines 14-52, and col. 9, line 39—col. 10, line 5].
- 17. As to claim 14, Sekiguchi discloses an information processing apparatus comprising: process save area generating means for saving, at generation of a user process, process information in the OS relating to the generated user process to a save area on a main memory device [col. 13, lines 14-52, and col. 9, line 39—col. 10, line 5]; process save information updating means for setting, at switching of the user process, a restart flag for the saved process information to designate whether the process is to be restarted or not, while updating the process information saved in the save area to the latest state if the process is not to be restarted [col. 10, lines 14-20]; process information save area releasing means for nullifying the saved process information, at termination of the user process [1004]; main memory initialization means for initializing, at restart of an OS, a main memory area of the main memory device used by the OS while not restarting the save area used by the user process for which the restart flag is set not to restart [col. 10, lines 6-10]; and process restoration means for restoring, after the restart of the

OS, the saved process information of the user process for which the restart flag is set not to restart, in the OS [col. 10, line 29 – col. 11, line 35].

- 18. As to claim 15, Sekiguchi discloses means for searching, when a restart flag is set for process information relating to a certain user process to designate whether the process is to be restarted or not, all the user processes belonging to the same user application program as the user process, and setting restart flags in the process information relating to all the searched user processes to the same value [col. 8, lines 19-37].
- 19. As to claim 16, Sekiguchi discloses means for searching, when a restart flag is set for process information relating to a certain user process to designate whether the process is to be restarted or not, all the user processes belonging to the user application program as the user process, and setting restart flags in the process information relating to all the searched user processes to the same value [col. 8, lines 19-37].
- 20. As to claim 17, Sekiguchi discloses means for starting up the OS from an OS main memory image stored in a nonvolatile storage portion forming a part of the main memory device [FIG. 1, "REBOOT LOADER" in main memory device 102].
- 21. As to claim 18, Sekiguchi discloses means for copying, at every occurrence of a write access from the OS to the nonvolatile storage portion during system operation, data in an address range having a predetermined width including the address at which the write access has occurred

from the nonvolatile storage portion to a substitute area ensured in a readable/writable main memory portion forming a part of the main memory device, and for converting subsequent accesses to the address range having the predetermined width to accesses to the substitute area [col. 9, line 39—col. 10, line 5].

- 22. As to claim 19, Sekiguchi discloses a program for causing a computer to function as: process information saving means for saving, before restart of an OS, process information in the OS relating to a user process to a save area on a main memory device [col. 13, lines 14-52, and col. 9, line 39—col. 10, line 5]; main memory initialization means for initializing, at the restart of the OS, a main memory area of the main memory device used by the OS while not restarting the save area used by the user process [col. 10, lines 6-10]; and process restoration means for restoring the saved process information in the OS after the restart of the OS [col. 10, line 29 col. 11, line 35].
- 23. As to claim 20, Sekiguchi discloses a program for causing a computer to function as: process information saving means for saving, before restart of an OS, process information relating to a user process to a save area on a main memory device [col. 13, lines 14-52, and col. 9, line 39—col. 10, line 5]; restart flag setting means for setting a restart flag for the saved process information to designate whether the process is to be restarted or not [col. 10, lines 14-20]; main memory initialization means for initializing, at the restart of the OS, a main memory area of the main memory device used by the OS while not restarting the save area used by the user process for which the restart flag is set not to restart [col. 10, lines 6-10]; and process

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restoration means for restoring, after the restart of the OS, the saved process information of the user process for which the restart flag is set not to restart in the OS [col. 10, line 29 – col. 11, line 35].

- 24. As to claim 21, Sekiguchi discloses a program for causing a computer to function as: process information saving means for saving, before restart of an OS, process information in the OS relating to a user process to be continuously operated after restart of the OS, to a save area on a main memory device [col. 13, lines 14-52, and col. 9, line 39—col. 10, line 5]; main memory initialization means for initializing, at the restart of the OS, a main memory area of the main memory device used by the OS while not restarting the save area used by the user process [col. 10, lines 6-10]; and process restoration means for restoring, after the restart of the OS, the saved process information in the OS [col. 10, line 29 col. 11, line 35].
- 25. As to claim 22, Sekiguchi discloses before the restart of the OS, the process information saving means saves process information in the OS relating to a user process to be continuously operated after restart of the OS, to the save area on the main memory device, by referring to a process ID table storing identifiers of processes to be continuously operated or of processes not to be continuously operated [col. 13, lines 14-52, and col. 9, line 39—col. 10, line 5].
- 26. As to claim 23, Sekiguchi discloses a program for causing a computer to function as: process save area generating means for saving, at generation of a user process, process information in the OS relating to the generated user process to a save area on a main memory

device [col. 13, lines 14-52, and col. 9, line 39—col. 10, line 5]; process save information updating means for setting, at switching of the user process, a restart flag for the saved process information to designate whether the process is to be restarted or not, while updating the process information saved in the save area to the latest state if the process is not to be restarted [col. 10, lines 14-20]; process information save area releasing means for nullifying the saved process information, at termination of the user process [1004]; main memory initialization means for initializing, at restart of an OS, a main memory area of the main memory device used by the OS while not restarting the save area used by the user process for which the restart flag is set not to restart [col. 10, lines 6-10]; and process restoration means for restoring, after the restart of the OS, the saved process information of the user process for which the restart flag is set not to restart in the OS [col. 10, line 29 – col. 11, line 35].

- 27. As to claim 24, Sekiguchi discloses means for searching, when a restart flag is set for process information relating to a certain user process to designate whether the process is to be restarted or not, all the user processes belonging to the same user application program as the user process, and setting restart flags in the process information relating to all the searched user processes to the same value [col. 8, lines 19-37].
- 28. As to claim 25, Sekiguchi discloses means for searching all the user processes belonging to the user application program as the user process, when a restart flag is set for process information relating to a certain user process to designate whether the process is to be restarted

or not, and setting restart flags in the process information relating to all the searched user processes to the same value [col. 8, lines 19-37].

- 29. As to claim 26, Sekiguchi discloses means for starting up the OS from an OS main memory image stored in a nonvolatile storage portion forming a part of the main memory device [FIG. 1, "REBOOT LOADER" in main memory device 102].
- 30. As to claim 27, Sekiguchi discloses means for copying, at every occurrence of a write access from the OS to the nonvolatile storage portion during system operation, data in an address range having a predetermined width including the address at which the write access has occurred from the nonvolatile storage portion to a substitute area ensured in a readable/writable main memory portion forming part of the main memory device, and for converting subsequent accesses to the address range having the predetermined width to accesses to the substitute area [col. 9, line 39—col. 10, line 5].
- As to claim 28, Sekiguchi discloses a method for restarting an OS in a computer in which a first OS memory area for loading an OS and a process memory area for loading processes are allocated on a main memory, and the OS and the processes are loaded in the respective areas, the OS restart method comprising: a first step of acquiring process information, that is information for the OS to manage the processes, from the first OS memory area and storing the same in a save area provided in a predetermined storage device [col. 13, lines 14-52, and col. 9, line 39—col. 10, line 5]; a second step of initializing the first OS memory area while holding the process

memory area [col. 10, lines 6-10]; a third step of allocating a second OS memory area on the main memory and loading the OS therein [col. 10, lines 6-10]; and a fourth step of updating the process information in the OS memory area according to the process information stored in the first step [col. 10, line 29 – col. 11, line 35].

- 32. As to claim 29, Sekiguchi discloses selecting a process to be held from the processes loaded in the process memory area [col. 10, lines 56-58]; and initializing the process memory area allocated to the processes not selected [col. 10, lines 56-58].
- 33. As to claim 30, Sekiguchi discloses the save area is provided on the main memory [102].
- 34. As to claim 31, Sekiguchi discloses information indicating whether each of the processes is to be restarted or not is stored in the save area together with the process information of the relevant process [FIG. 6].
- 35. As to claim 32, Sekiguchi discloses information indicating whether each of the processes is to be restarted or not is stored in a separate storage device from the storage device having the save area provided therein [105].
- 36. As to claim 33, Sekiguchi discloses processing to generate, update and release the save area are executed in accordance with the generation, switching and termination of a process on

the storage device having the save area provided therein [col. 11, lines 21-28].

37. As to claim 34, Sekiguchi discloses preliminarily preparing a nonvolatile storage device

storing an image of the OS when it is loaded in the main memory, the third step referring to the

image stored in the nonvolatile storage device to load the OS in the main memory [111].

38. As to claim 35, Sekiguchi discloses loading a process including a plurality of processes

associated with one application program in the process memory area; selecting a process to be

held from among the processes loaded in the process memory area; and initializing the process

memory area allocated to the processes other than the selected process and the other processes

associated with the same application program as the selected process [col. 10, line 29 – col. 11,

line 35].

### Response to Arguments

39. Applicant's arguments filed April 19, 2010 have been fully considered but they are not

persuasive.

40. In the remarks, applicants argued in substance that Sekiguchi does not teach or suggest

saving process information in the OS to a save area and restoring the saved process information

in the OS after the start of the OS. But Sekiguchi teaches saving process information about non-

stop modules, inter alia, states of virtual addresses, the address translation table, and other

resource management data [col. 5, lines 5-15], substantially as claimed.

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41. In the remarks, applicants argued in substance that the non-stop module taught by Sekiguchi is executed independently of the OS and is therefore not part of the OS. But Sekiguchi teaches that the non-stop modules comprise device drivers for use with an OS [col. 1, lines 15-26]. Although it is true that the non-stop module *operates independently* of normal operation of the OS, they are nonetheless *part of* the OS; the RTX Real-Time Subsystem is a part of Windows NT [col. 1, lines 33-45]. Thus, process information pertaining to a non-stop module is process information that is in the OS, substantially as claimed.

#### Conclusion

42. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

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43. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to ERIC CHANG whose telephone number is (571)272-3671. The

examiner can normally be reached on M-F 9:00-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Thomas Lee can be reached on (571) 272-3667. The fax phone number for the

organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would

like assistance from a USPTO Customer Service Representative or access to the automated

information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Eric Chang/

Examiner, Art Unit 2116

/Thomas Lee/

Supervisory Patent Examiner, Art Unit 2115